

TASys

COLLABORATORS

	<i>TITLE :</i> TASys		
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Chapter 1

TASys

1.1 TASys - Main - (C) S.Gillibrand 1995 - Digital Design

```
TASys
System Information BBS Utility For TransAmiga BBS S/W ((C) S.Radwan)
```

```
Version 1.0
(C) Copyright 1995 S.Gillibrand - Digital Design
```

```
FREWARE
```

```
What Is TASys?
```

```
System Requirements
```

```
How Do I Install TASys?
```

```
Usage
```

```
Note For 040 Users
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Known Bugs
```

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History
```

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How To Contact The Author
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What/Who Is Digital Design?
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1.2 TASys - What Is TASys? - (C) S.Gillibrand 1995 - Digital Design

```
What Is TASys?
+-----+
```

```
TASys is a small TransAmiga programme to give the user some information
about your system... It lists the following information:
```

```

\textdegree{} \ensuremath{\pm} $^2$ TA-Sys v1.0 - S.Gillibrand 1995 - Digital ←
Design Production 1995 Û$^2$ \ensuremath{\pm} \textdegree{}
Û$^2$ $^2$ \ensuremath{\pm} \ensuremath{\pm} \textdegree{} \textdegree{} Memory Status- ←
Chip: Fast: Total: \textdegree{} \textdegree{} \ ←
\ensuremath{\pm} \ensuremath{\pm} $^2$ $^2$ Û
$^2$ $ ←
^2$
\ensuremath{\pm} Cpu Speed: Mhz Copyback ←
Mode: \ensuremath{\pm}
\textdegree{} Instruction ←
Cache: \textdegree{}
CPU Type: DMA/GFX Chip: Instruction Burst:
MMU Type: Display Mode: Data Cache:
FPU Type: Display Chip: Data Burst:

CPU Mips:
FPU Mips: Card Slot Installed?:

Hardware Clock Status: Dhrystones Per Second:
VBR Address:
Ramsey Chip Rev. (A3000): [Speed Tests]
Gary Chip Rev. (A3000):
VBlank Frequency (Hz): A1200 - 14Mhz:
Power Supply Frequency (Hz): A3000 - 25Mhz:
\textdegree{} Horizontal Frequency (KHz): A4000 - 25Mhz: ←
\ensuremath{\pm} \textdegree{}
\ensuremath{\pm} EClock Frequency (Hz): ←
\ensuremath{\pm}
$^2$ $ ←
^2$
Û$^2$ $^2$ \ensuremath{\pm} \ensuremath{\pm} \textdegree{} \textdegree{} ←
SysInfo System Comment: \textdegree{} \textdegree{} ←
{} \ensuremath{\pm} \ensuremath{\pm} $^2$ $^2$ Û

```

Aswell as displaying your mounted DEVICES - ACTIVE TASKS and MEMORY STATUS

Unfortunatley Non-Ansi users will still have to put up with the ANSI Positioning codes but they get a Black&White screen instead of the standard colour screen.

It takes approximatley 8 seconds to gather all this info so the users phonebill won't be damaged too much *;p.

TASys has internal error handling (SIGNAL ON SYNTAX) so it won't just quit without giving you a reason if something was to go wrong.

1.3 TASys - System Requirements - (C) S.Gillibrand 1995 - Digital Design

System Requirements
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- 1) An Amiga ;-)< Why is it EVERY guide has this in - It's not funny ;0>
- 2) TransAmiga BBS Software v1.x ->

3) SysInfo (By Nic Wilson - Available on Aminet/Any Good BBS)

4) ARexx Server Running

5) The Following Commands In Your C DIR:

```
C:Avail, C:Status, C:QInfo
```

I personally use v3.24 of SysInfo but I think any version 3 or later will work - v3.24 is available on my BBS for download.

Note: Problems may occur in later versions than 3.24 due to the author (Nic Wilson) fixing a bug in SysInfo, if this occurs I'll release TASys v1.1 - Which will be available here and on the Aminet/Other good bbs's.

1.4 TASys - How To Install TASys - (C) S.Gillibrand 1995 - Digital Design

How Do I Install TASys?

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Follow these easy steps and you'll have no problems.

1) Copy REXX/TASYS.TRANS to BBS:REXX (Or your BBS REXX path).

2) Edit with a text editor (CygnumEd/GoldEd/Etc.) TASys.Trans and change the paths to reflect your system's setup.

```
/* Variables To Alter */
```

```
sysinfopath="Work:Utilities/SysInfo/SysInfo" /* The Path To The SysInfo */
qinfopath="C:Qinfo" /* The Path To The QInfo */
statuspath="C:Status" /* The Path To The Status */
availpath="C:Avail" /* The Path To The Avail */
cpu=0 /* 0=CPU ISN'T a 68040 */
/* 1=CPU IS a 68040 */
```

```
/* */
```

The above are the lines you should edit in the TASys.Trans

3) Edit BBS:CONFIGS/MAIN.RX (Or whichever menu you choose) to include an entry for TASys something like this:

```
T TA-Sys          10 -----X TASys.Trans
```

Read the TransAmiga manual for more info.

4) Locally logon to your BBS and run it to see the great work I have done :^)

Note: If you're using an Amiga with a Motorola 68040 processor please read

Note For 040 Users
!

1.5 TASys - Usage - (C) S.Gillibrand 1995 - Digital Design

Usage
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Self explanatory really!

When a user is online, if they select to run TASys then it will run and function as it was made to.

Sorry this is a bit of a needless note ;).

1.6 TASys - Note For 040 Users - (C) S.Gillibrand 1995 - Digital Design

Note For 040 Users
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If you run an Amiga with a Motorola 68040 processor inside then note:
You should have a 040.library in your LIBS: dir.

If you do then you must also edit Line 10 of TASys.Trans to reflect this.

This is due to a bug in Sysinfo which I have no control over.

1.7 TASys - Known Bugs - (C) S.Gillibrand 1995 - Digital Design

Known Bugs
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None.

1.8 TASys - History - (C) S.Gillibrand 1995 - Digital Design

History
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*Version 1.0

Date Details

27-Sep-95 First Public Release Of TASys.

1.9 TASys - How To Contact The Author - (C) S.Gillibrand 1995 - Digital Design

 How To Contact The Author

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Please do contact me for
 Bug Reports
 or
 Improvement
 ideas that you have.

You can contact me via the following methods:

BBS:

Digital Dreams Amiga BBS UK - 44-1772-454995 - 24 Hours

NETMAIL:

2:250/602.0

E-MAIL:

scatman@digitald.demon.co.uk
sysop@digitald.demon.co.uk

SNAIL MAIL:

Stuart Gillibrand,
8, Thornton Drive,
Farington Moss,
Leyland,
Preston,
Lancashire.
PR5 3QH
ENGLAND

Don't hesitate to contact me no matter how trivial the matter.

1.10 TASys - The Famous Digital Design! - (C) S.Gillibrand 1995 - Digital Design

 What/Who is Digital Design

+-----+

Digital Design is a group of people who code all types of programmes for BBSs around the world, TransAmiga, DLG, Xeno, ANY Basically.

It was founded by

Stuart Gillibrand
in February 1995.

President: Stuart Gillibrand - scatman@digitald.demon.co.uk
Vice President: Jamie Prince - cosysop@digitald.demon.co.uk

Programmers:
Stuart Gillibrand
Jamie Prince

ANSI Artists:
Stuart Gillibrand
Jamie Prince

Documentation writers:
Stuart Gillibrand
Jamie Prince

Ideas:
Stuart Gillibrand
Jamie Prince

If you would like to join Digital Design as a Programmer, Doc Writer, Artist
or Idea Conceptor (!?) then contact

ME

.

We ONLY accept programming in the languages:

Arexx, C, E and ASM.

You will need to show us an example of your skill if you wish to join, which
both Jamie and I will look over before contacting you.
